PICJUG - Task #460

Task # 458 (New): Schematic/layout updates for v1.1

Add level translator and run the PIC at VUSB (+5V) all the time

10/31/2013 01:30 PM - Andrew Kohlsmith

Status:	New	**Start date:**	10/31/2013
Priority:	Normal	**Due date:**	
Assignee:		**% Done:**	0%
Category:		**Estimated time:**	0:00 hour
Target version:			

Description

The PIC needs to run from 3.3V at a minimum in order to be able to run at 48MHz recovered from the USB. This means that all our I/O must go through level translators to ensure that we can get 1.2, 2.5 and 3.3V I/O.

Related issues:

Related to Bug #442: low voltage variants will require external 3.3V regulator	**New**	**10/01/2013**